

J ⊤ R S International Journal of Technical Research & Science OPERATIONAL TRANSCONDUCTANCE AMPLIFIER FOR BIOMEDICAL EMG APPLICATION USING CMOS 90nm TECHNOLOGY

Bijesh Kumar Singh, Gori Shankar, Bharat Bhushan Jain E-Mail Id: gssharmajec@gmail.com, bijeshsingh5125@gmail.com, drbharatjainjec@gmail.com Department of Electronics & Communication. Jaipur Engineering College, Kukas, Jaipur, Rajasthan, India

Abstract- The universe of biomedical gadgets is growing quickly with new activities and new innovations. As of now biomedical gadgets are fabricated with countless capacities, exactness, precision, minimization and usability. In portable biomedical devices, power consumption has become a serious problem due to battery life. The operational transconductance amplifier is a mostly used analog processing block. In recent years, the development of OTA with very low conductivity, low power, low voltage and improved linearity has been mainly used in biomedical applications. In this paper present a novel OTA design for biomedical EMG application with operating frequency of this application is 150Hz. The proposed design is simulated using CMOS 90nm environment with power supply of VDD is 0.35V and VSS is -0.35V.

Keywords: OTA, EMG, Biomedical Device, Low Power, CMOS Environment.

1. INTRODUCTION

Due to the rapid and numerous developments of microelectronics in recent years, more and more applications require a very small amplitude signal measuring module, for example, implantable devices in biomedical applications [1]. Monitoring various biomedical signals of the human body is a very interesting topic, because it helps to reveal important information about human health from the collected data. Doctors use this data to diagnose diseases [2]. Biomedical signals such as electrocardiogram (ECG), electromyogram (EMG), electroencephalogram (EEG) [3]. The implementation of the low-frequency design is not easy to design, since the Gm value requirement is in the nA / V range, and the capacitor value is greater than 100pF. Many foundries cannot provide a capacitor value of more than 50 pF, also due to the large area of a large-scale condenser, which cannot be easily realized. Another problem with low nA / V steepness is worsened by noise, distortion, and imperfection [4].

2. BIOMEDICAL SIGNAL PROCESSING SYSTEM

Biomedical signs are perceptions of the physiological exercises of living beings, going from the succession of qualities and proteins, to nerve and cardiovascular rhythms, to tissue and organ pictures. The reason for biomedical sign handling is to separate significant data from biomedical signs. With the assistance of biomedical sign handling, scholars can find new science and specialists can handle different sicknesses. A typical biomedical signal processing system is shown in Fig. 2.1. Analog processing blocks are the most important part of biomedical systems. In this preamplifier and filter are the two most important analog processing blocks. Since the biomedical signal operates at a very low frequency, low pass filter is most commonly used for biomedical signal processing. OTA is one of the most important blocks for filter design [5].



Fig. 2.1 General Purpose Biomedical System Block Diagram

The preamplifier amplifies the input signal to a higher level with very low noise and distortion. In an electrocardiograph application (ECG application) where the amplitude of a preamplifier signal is processed by a low-pass filter at a rate of about 100 mV, the high efficiency of biomedical filters or ultra short frequency filters is a CMOS technology [6].

DOI Number: https://doi.org/10.30780/IJTRS.V06.I10.004

pg. 18

Paper Id: IJTRS-V6-I10-005

www.ijtrs.com, www.ijtrs.org Volume VI Issue X, October 2021

@2017, IJTRS All Right Reserved



International Journal of Technical Research & Science Table-2.1 Most Commonly Used Frequency for Biomedical Signals [7]

Signal	Frequency
ECG (Electrocardiography)	250Hz
EEG (Electroencephalography)	200Hz
EMG (Electromyography)	150Hz
ERG (Electroretinography)	100Hz

3. OPERATION TRANSCONDUCTANCE AMPLIFIER (OTA)

OTA an amplifier whose differential input voltage generates an output current [7]. Therefore, it is the voltage control current source. Normally, a current input for controlling the current conversion of the amplifier is added. The OTA has a high impedance input phase and is similar to a standard op amp in that it can be used for negative feedback [8]. Gilbert Multiplier cell is utilized as OTA which is recently utilized in immersion area as a multiplier, yet in sub limit locale its transconductance is characterized by tanh geometrical personality and in this plan OTA is work on the sub edge district [9].



Fig 2: Multi-tanh Doublet Linearity Technique

To increase the linear range of the circuit, the multi-tanh doublet technique is used. In the figure 2, the differential pair represented by sets M1 - M2 & M3 - M4 shown a voltage offset with the same absolute value but in opposite direction of transfer characteristics. I₁, I₂, I₃ and I₄ are the current of transistors M₁, M₂, M₃ and M₄ respectively.

$$I_1 - I_2 = I_B \tanh\left[\frac{V_{in} + V_{oS}}{2n\phi_t}\right]$$
(1)

And

$$I_3 - I_4 = I_B \tanh\left[\frac{V_{in} - V_{oS}}{2n\phi_t}\right]$$
⁽²⁾

The output current of this circuit is given by:

$$I_{out} = (I_1 - I_2) + (I_3 - I_4)$$
(3)

Now, from using the eq. (1) and eq. (2) the final output current of the circuit is

$$I_{out} = I_B \{ \tanh\left[\frac{V_{in} + V_{oS}}{2n\phi_t}\right] + \tanh\left[\frac{V_{in} - V_{oS}}{2n\phi_t}\right] \}$$
(4)

Using this technique we increase the linearity of the circuit and also the power consumption of the circuit is very low using this technique.

DOI Number: https://doi.org/10.30780/IJTRS.V06.I10.004

pg. 19

Paper Id: IJTRS-V6-I10-005

www.ijtrs.com, www.ijtrs.org Vo @2017, IJTRS All Right Reserved

Volume VI Issue X, October 2021



International Journal of Technical Research & Science
DESIGN AND SIMULTION

4. PROPOSED DESIGN AND SIMULTION

The proposed OTA design for biomedical ECG application is shown in the fig. 4.1. This OTA is design using CMOS 90nm environment with supply voltage VDD = +0.35V and VSS = -0.35V.



Fig. 4.1 Proposed OTA design for EMG application

The fig. 4.2 shown the output current from each input transistor (M1, M2, M3 and M4) of OTA for EMG Application and in figure 5 shown the overall output current of the proposed OTA for EMG application.



Volume VI Issue X, October 2021

@2017, IJTRS All Right Reserved

www.ijtrs.com, www.ijtrs.org



The overall transconductance response of the proposed OTA is shown in the Fig. 4.4. The overall transconductance for the EMG application is obtained 38.64 nS. The frequency response of the proposed OTA with control voltage Vc = -0.155V is obtained 150Hz that is used for the biomedical EMG application. The frequency response of proposed OTA for EMG application is shown in the Figure 7. The power consumption of the OTA for EMG application is obtained 4.15nW.



@2017, IJTRS All Right Reserved

I J I K > International Journal of Technical Research & Science The complete summary of the proposed OTA design for biomedical EMG application is shown in the table 4.1.

Parameters	Value
CMOS Environment	90nm
Voltage Supply	±0.35V
Consumption of Power	4.15 nW
Total Current	9.98 nA
Gm	38.64 nS
Vc	-0.155 V
Application	EMG
Tuning Method	By varying Vc

CONCLUSION

In this paper present a novel OTA design for biomedical EMG application with operating frequency of this application is 150Hz. The proposed design is simulated using CMOS 90nm environment with power supply of VDD is 0.35V and VSS is -0.35V. In proposed OTA the value of the control voltage Vc = -0.155V is used for the biomedical EMG application. In this application the transconductance of the proposed OTA is 38.64 nS, frequency response of the OTA is 150Hz and the power consumption of the proposed OTA is 4.15nW.

REFERENCES

- G. K. Soni, H. Singh, H. Arora and A. Soni, "Ultra Low Power CMOS Low Pass Filter for Biomedical ECG/EEG Application," IEEE 2020 Fourth International Conference on Inventive Systems and Control (ICISC), PP-558-561, 2020.
- [2] Bijesh Kumar Singh, Gori Shankar, Dr. Bharat Bhushan Jain. "An Overview on Low Voltage Low Power Operational Transconductance Amplifier OTA for Biomedical Application". International Journal of Engineering Trends and Applications (IJETA) Vol-8(3), pp. 1-5, May - Jun 2021.
- [3] Rajeev Kumar, Sanjeev Sharma and Amandeep Singh, " A Low Power Low Transconductance OTA in 180nm CMOS Process For Biomedical Applications", IJCTA, vol-9, issue- 11, PP- 5427-5434, 2016.
- [4] S. Gour and G. K. Soni, "Reduction of Power and Delay in Shift Register using MTCMOS Technique," IEEE 2020 4th International Conference on Trends in Electronics and Informatics (ICOEI), PP-202-206, 2020.
- [5] Shuenn-Yuh Lee, Cheng-Pin Wang and Yuan-Sun Chu, "Low-Voltage OTA–C Filter With an Areaand Power-Efficient OTA for Biosignal Sensor Applications", IEEE Transactions On Biomedical Circuits And Systems, Vol-13, No-1, PP-56-67, February 2019.
- [6] Himadri Singh Raghav, B.P. Singh and Sachin Maheshwari," Design of Low Voltage OTA for Bio-medical Application", IEEE International Conference on Microelectronics, Communication and Renewable Energy (ICMiCR-2013), PP-1-5, 2013.
- [7] Mr. Gaurav Kuamr Soni, Mr. Kamlesh Gautam, Mr. Kshitiz Agarwal, "Flipped Voltage Follower Based Operational Transconductance Amplifier for High Frequency Application", International Journal of Advanced Science and Technology, Vol. 29, No. 9s, pp-8104-8111, 2020.
- [8] Gaurav Kumar Soni And Gouri Shankar Sharma, "A Review on Low Voltage Low Power Gm-C and OTA-C Low Pass Filter for Biomedical Application", International Journal of Engineering and Technical Research (IJETR), Volume-4, Issue-2, PP-33-36, February 2016.
- [9] Gaurav Kumar Soni and Himanshu Arora "Low Power CMOS Low Transconductance OTA for Electrocardiogram Application", Springer International Conference on Recent Trends in Communication & Intelligent System, Algorithms for Intelligent Systems, PP- 63-69, 2020.

Volume VI Issue X, October 2021

www.ijtrs.com, www.ijtrs.org